

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Wolrich et al. Art Unit: 2187
Serial No.: 10/780,330 Examiner: Prasith Thammavong
Filed : February 17, 2004 Assignee: Intel Corporation
Title : MEMORY MAPPING IN A PROCESSOR HAVING MULTIPLE
PROGRAMMABLE UNITS (AS AMENDED)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Please correct the Filing Receipt for the above-referenced application to include the correct assignment information as follows:

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Please supply a corrected Filing Receipt to the undersigned with respect to this application. A copy of the original Filing Receipt showing the desired changes in red ink is attached for your convenience.

No fee is believed to be due. If, however, there are any charges or credits, please apply them to Deposit Account No. 06-1050.

Respectfully submitted,

Date: June 8, 2007

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10/780,330	02/17/2004	2127	770	10559-127002 / P7866C/I		10	16

CONFIRMATION NO. 1102

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UPDATED FILING RECEIPT

 OC000000012979686

Date Mailed: 06/17/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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REPORTED
TO INTEL

Assignment For Published Patent Application

Intel Corporation, a California corporation;
 Delaware

Domestic Priority data as claimed by applicant

This application is a CON of 09/473,271 12/27/1999 PAT 6,694,380

Foreign Applications

If Required, Foreign Filing License Granted: 05/08/2004

Projected Publication Date: 09/23/2004

Non-Publication Request: No

Early Publication Request: No

<input checked="" type="checkbox"/> Cocketing Required *
Reviewed By Practice Systems
Attala: <i>am</i>
Reviewed By Billing Secretary
Ma's: <i>LMB</i>

Title

Memory mapping in a multi-engine processor

Preliminary Class

718

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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